



Docket No.: M4065.0536/P536-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Hongmei Wang et al.

Application No.: 10/751,443

Confirmation No.: 2565

Filed: January 6, 2004

Art Unit: 2811

For: FULLY-DEPLETED (FD) (SOI) MOSFET
ACCESS TRANSISTOR AND METHOD
OF FABRICATION

Examiner: Not Yet Assigned

*Fee
Only*

BEST AVAILABLE COPY

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 C.F.R. § 1.97(b)(3)). Copies of the references on the PTO/SB/08 are not provided.

Those documents which are marked with a double asterisk (**) next to the Cite No. in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application number 10/161,615, filed